

10/031000

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U.S. UTILITY Patent Application

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| **APPLICANTS: Tsukahara Norihito; Akiguchi Takashi; Miyakawa Hiidenori; | | | | | |
| **CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/04599 07/13/2000 | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/202047 07/16/1999 JAPAN 2000-63686 03/08/2000 | | | | | |
| PG-PUB DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | | | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | ATTORNEY DOCKET NO | | | |
| 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | 2002-0024A | | | |
| Verified and Acknowledged Examiners's initials | | | | | |
| TITLE : Package of semiconductor device and method of manufacture thereof | | | | | |
| U.S. DEPT. OF COMM./PAT. & TM.-PTO-426L (Rev. 12-94) | | | | | |

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| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | | |
| | | Assistant Examiner | |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Total Claims | Print Claim for O.G. |
| | | Sheets Drwg. | Figs. Drwg. |
| | | | Print Fig. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Primary Examiner | |
| | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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